



PTO/SB/08A (10/01) (Substitute for form 1449A/PTO) INFORMATION DISCLOSURE STATEMENT BY APPLICANT (Use several sheets if necessary) Sheet <u>1</u> of <u>1</u>		ATTY. DOCKET NO. 01-CA-341		APPLICATION NO. 10/616,691			
		FIRST NAMED INVENTOR: Stefano SAGGINI					
		FILING DATE July 9, 2003		ART UNIT 2838			
U.S. PATENT DOCUMENTS							
Examiner Initials	Cite No.	Document No. No. - Kind Code	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Doc	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear		
FOREIGN PATENT DOCUMENTS							
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OTHER PRIOR ART - NON PATENT LITERATURE DOCUMENTS							
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A3		SAGGINI, et al. "A low-complexity high-performance digital control architecture for Voltage Regulator Modules" Proceedings of the 34 th IEEE Power of Electronics Specialist Conference, PESC. 2003, Vol. 1, June 15-19, 2003, Mexico, pp. 121-126.					
EXAMINER SIGNATURE A. Berhane				DATE CONSIDERED 11/04			
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